

02/10/2005 15:33

LSI LOGIC LEGAL IP → 917037464000

FEB 10 2005

## PART B - FEE(S) TRANSMITTAL

NO. 377 003

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LSI LOGIC CORPORATION  
1621 BARBER LANE  
MS: D-106  
MILPITAS, CA 95035

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I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO (703) 746-4000, on the date indicated below.

Mark Salvatore *[Signature]* (Depositor's name)  
*[Signature]* (Signature)  
February 10, 2005 (D)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/761,172	01/16/2001	William J. Dally	2789.2009-000	4912

## TITLE OF INVENTION: MULTISTAGE DIGITAL CROSS CONNECT WITH INTEGRAL FRAME TIMING

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1370	\$300	\$1670	03/03/2005
EXAMINER	ART UNIT	CLASS-SUBCLASS			
NGUYEN, BRIAN D	2661	370-369000			

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.  
 "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required.

2. For printing on the patent front page, list

(1) the names of up to 3 registered patent attorneys or agents OR, alternatively,  
(2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

Hamilton, Brook, Smith & Reynolds, P.C.

2 \_\_\_\_\_  
3 \_\_\_\_\_

## 3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

## (A) NAME OF ASSIGNEE

## (B) RESIDENCE: (CITY and STATE OR COUNTRY)

LSI Logic Corporation

Milpitas, California, USA

Please check the appropriate assignee category or categories (will not be printed on the patent):  Individual  Corporation or other private group entity  Government

## 4a. The following fee(s) are enclosed:

Issue Fee  
 Publication Fee (No small entity discount permitted)  
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A check in the amount of the fee(s) is enclosed.  
 Payment by credit card. Form PTO-2038 is attached.  
 The Director is hereby authorized to charge the required fee(s), or credit any overpayment. Deposit Account Number 12-2252 (enclose an extra copy of this form).

## 5. Change in Entity Status (from status indicated above)

a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.

b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

The Director of the USPTO is requested to apply the Issue Fee and Publication Fee (if any) or to re-apply any previously paid issue fee to the application identified above.

NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant; a registered attorney or agent; or the assignee or other party in interest as shown by the records of the United States Patent and Trademark Office.

Authorized Signature Henry GrothDate 1/31/2005Typed or printed name Henry GrothRegistration No. 39,696

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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LSI LOGIC

**FAX**Date February 10, 2005Number of pages including cover sheet: 3

**LSI Logic Corporation**  
**1621 Barber Lane**  
**MS: AD-106, Legal**  
**Milpitas, CA 95035**

**BEST AVAILABLE COPY****To: USPTO/Office of Publications**Phone (703) 305-8283Fax Phone (703) 746-4000

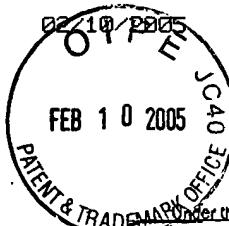
CC: \_\_\_\_\_

**From: Mark Salvatore**Intellectual Property  
ParalegalPhone (408) 433-7472Fax Phone (408) 433-7460**REMARKS:**

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Attention: Issue Fee Department

Re: 09/761,172



15:33

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NO. 377 D002

PTO/SB/21 (09-04)

Approved for use through 07/31/2008. OMB 0651-0031  
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

## TRANSMITTAL FORM

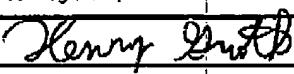
(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

Application Number	09/781,172
Filing Date	January 16, 2001
First Named Inventor	Dally, William J.
Art Unit	2681
Examiner Name	Nguyen, Brian D.
Total Number of Pages in This Submission	2
Attorney Docket Number	04-0505

ENCLOSURES (Check all that apply)		
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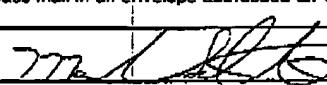
### SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	LSI Logic Corporation		
Signature			
Printed name	Henry Groth		
Date	1/31/2005	Reg. No.	39,698

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Mark Salvatore

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